

PROJECT NAME : NFL-C 15 CTL51  
PCB NO : LA-E841P

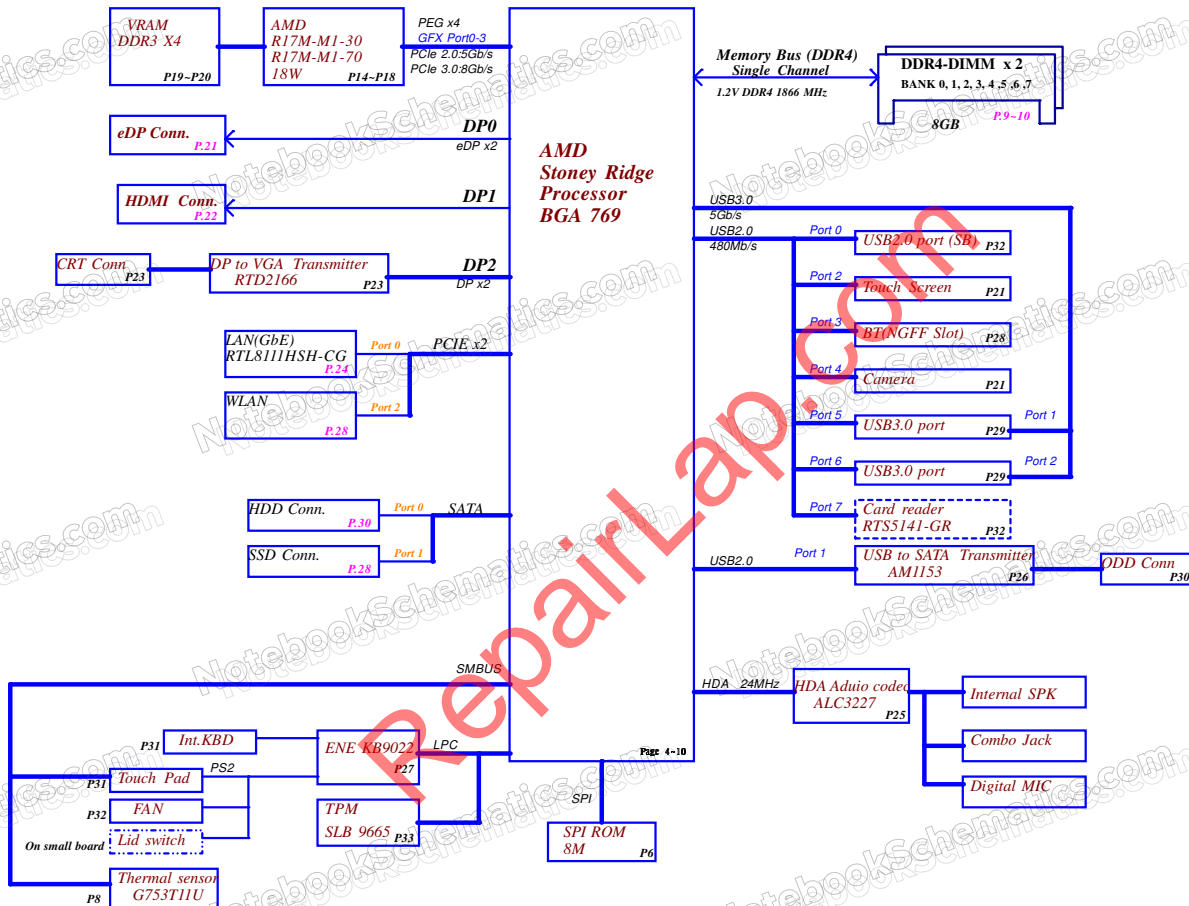
# Compal Confidential

## Schematic Document

AMD Stoney Ridge  
AMD R17M-M1-30/70

2016/10/12

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Issued Date	2014/06/10	Deciphered Date	2015/06/30	Title	<b>Cover Page</b>	
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				NFL-C CTL51 LA-E841P		
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Issued Date	2014/06/10	Deciphered Date	2015/06/30	Title	Block diagram
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Drawn by: NFL-CCTLS1 LA-E84IP				Sheet	2 of 48

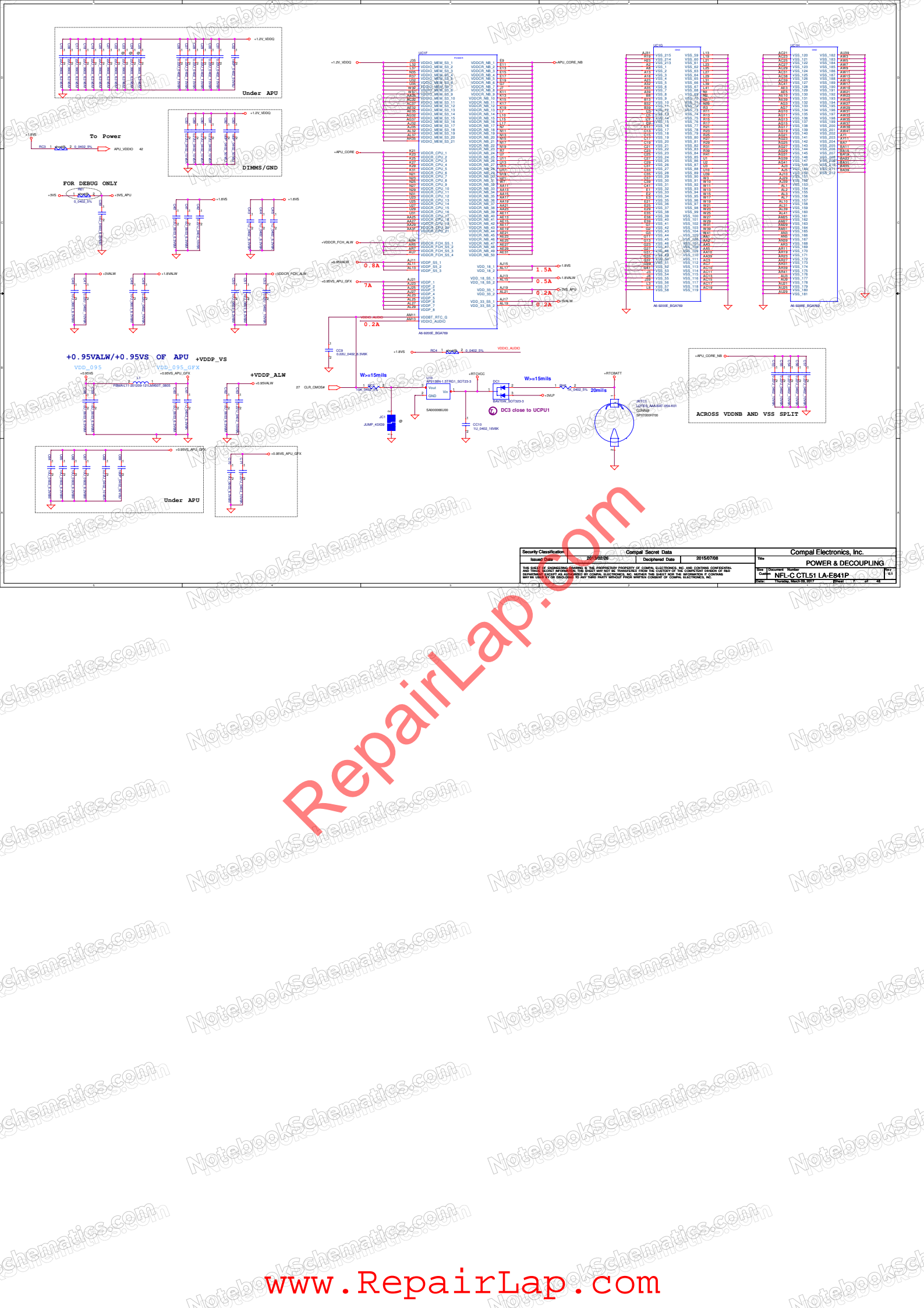










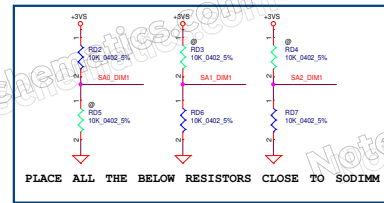


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Part No.	NFL-C CTL51 LA-E81P	Rev	1	Page	1 of 1
Model	Model No. 2015	Rev	1	Page	1 of 1





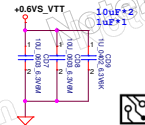
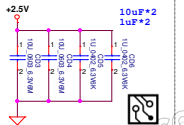
# Stoney JDIMM1 REVERSE TYPE



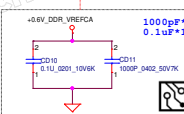
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Layout Note:  
Place near JDIMM1.257,259

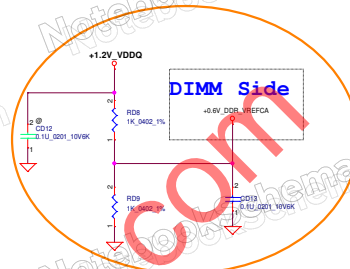
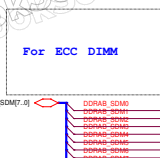
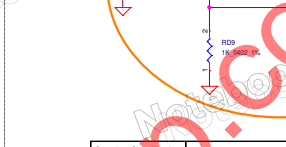
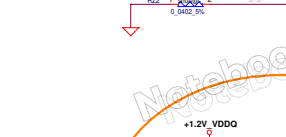
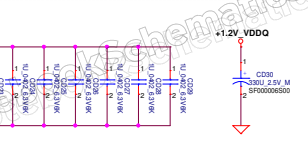
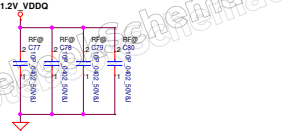
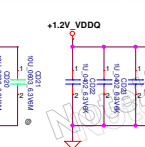
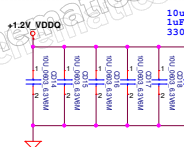
Layout Note:  
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Layout Note:  
Place the CAP near JDIMM1.164

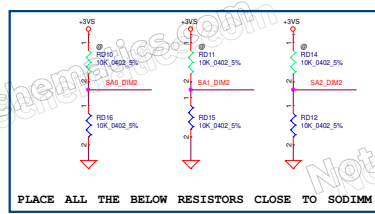


Layout Note:  
Place near JDIMM1



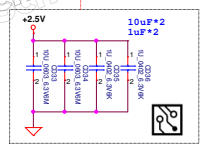
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Title		P11-DDR4 CHB: JDIMM1
Rev		Rev 5.1
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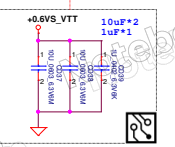


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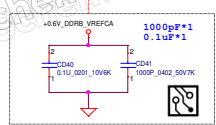
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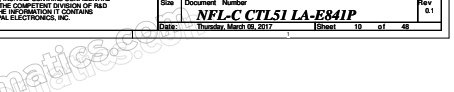
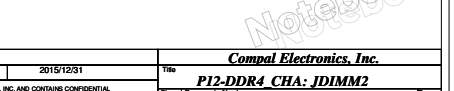
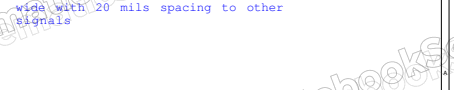
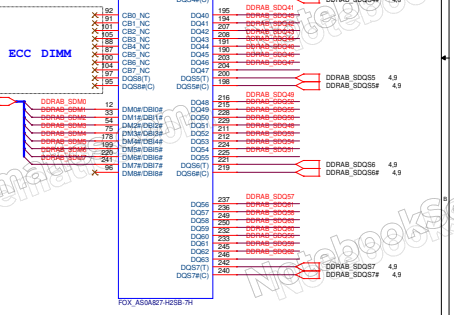
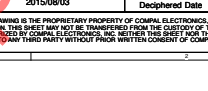
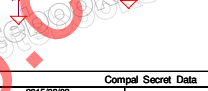
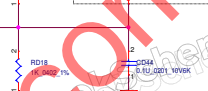
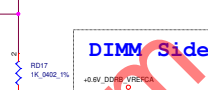
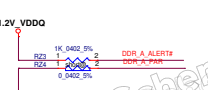
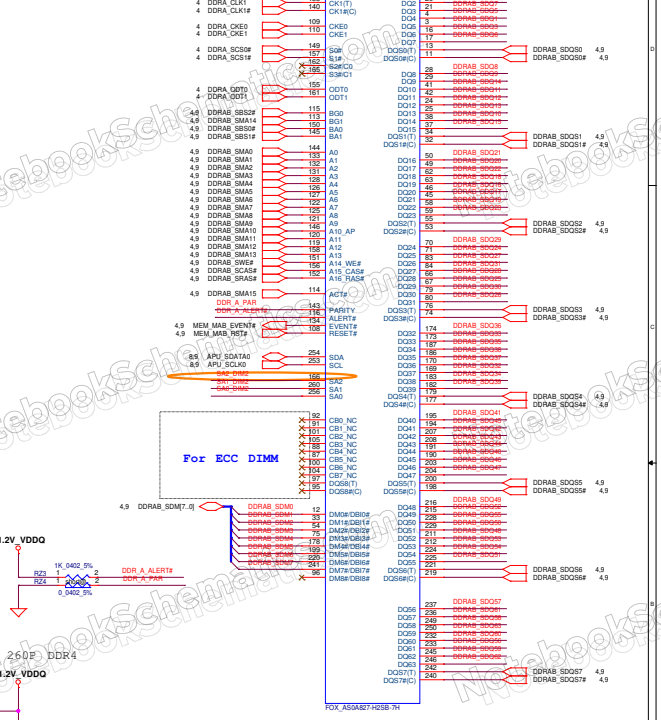
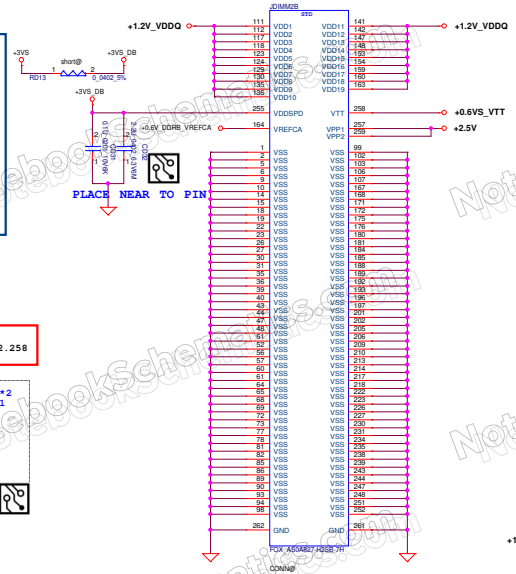
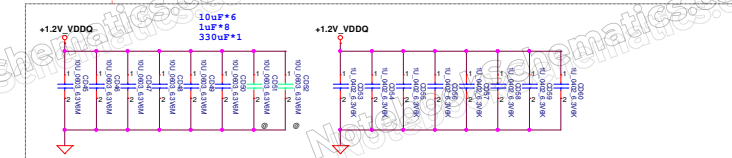
Layout Note:  
Place near JDIMM2.258



Layout Note:  
PLACE THE CAP WITHIN 200 MILS FROM THE JDIMM2



Layout Note:  
Place near JDIMM2



Part Number: LTCX0069FA0  
Part Value: S SOCKET FOX AS0A827-H2SB-7H 260P DDR4

DIMM Side

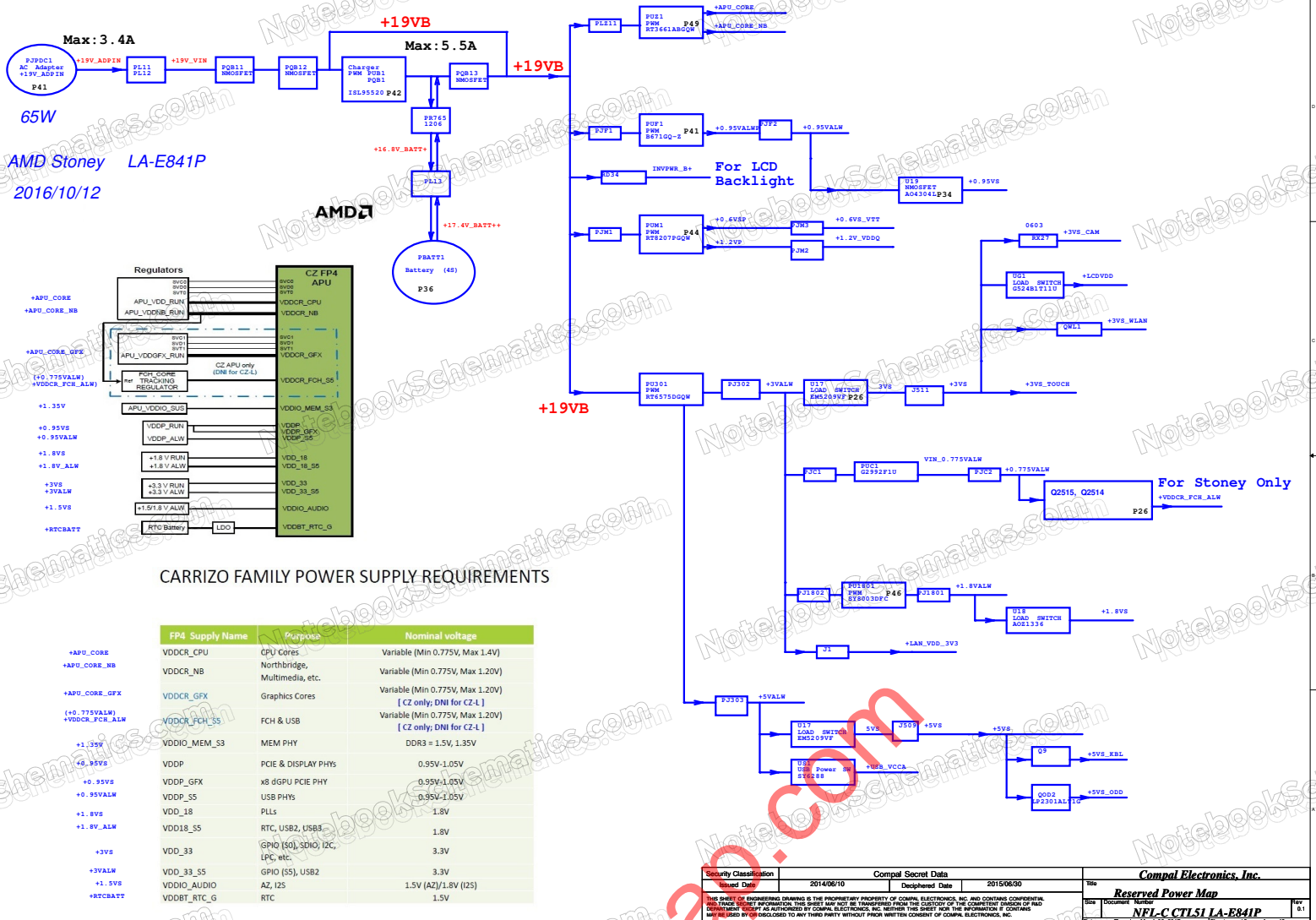
VREF traces should be at least 20 mils wide with 20 mils spacing to other signals

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Title		
P12-DDR4 CHA: JDIMM2		
Rev		
Rev 5.1		
Date		
Thursday, March 26, 2017		
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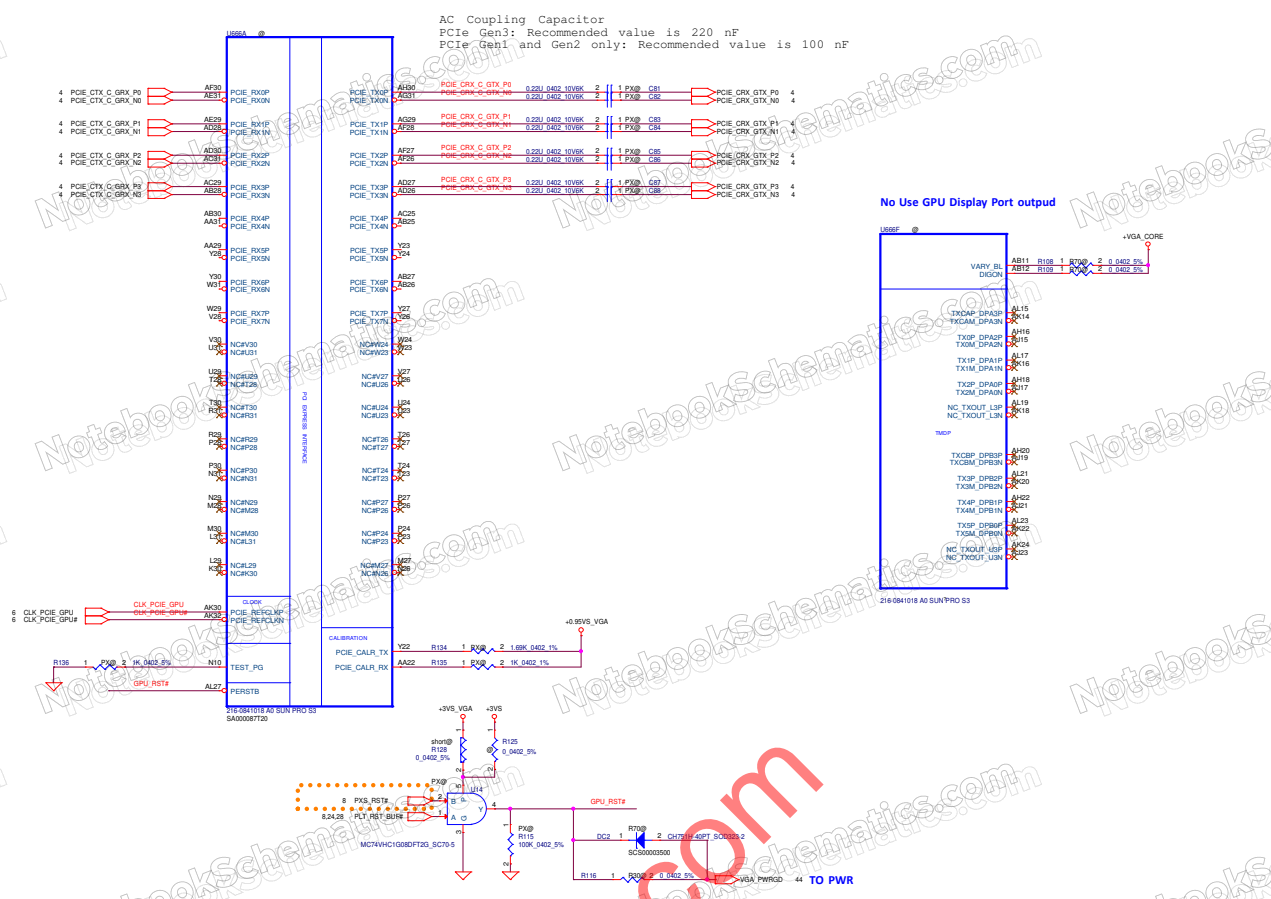
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		Date:		Thursday, March 09, 2017	
				Rev. 0.1	
				Sheet 12 of 48	

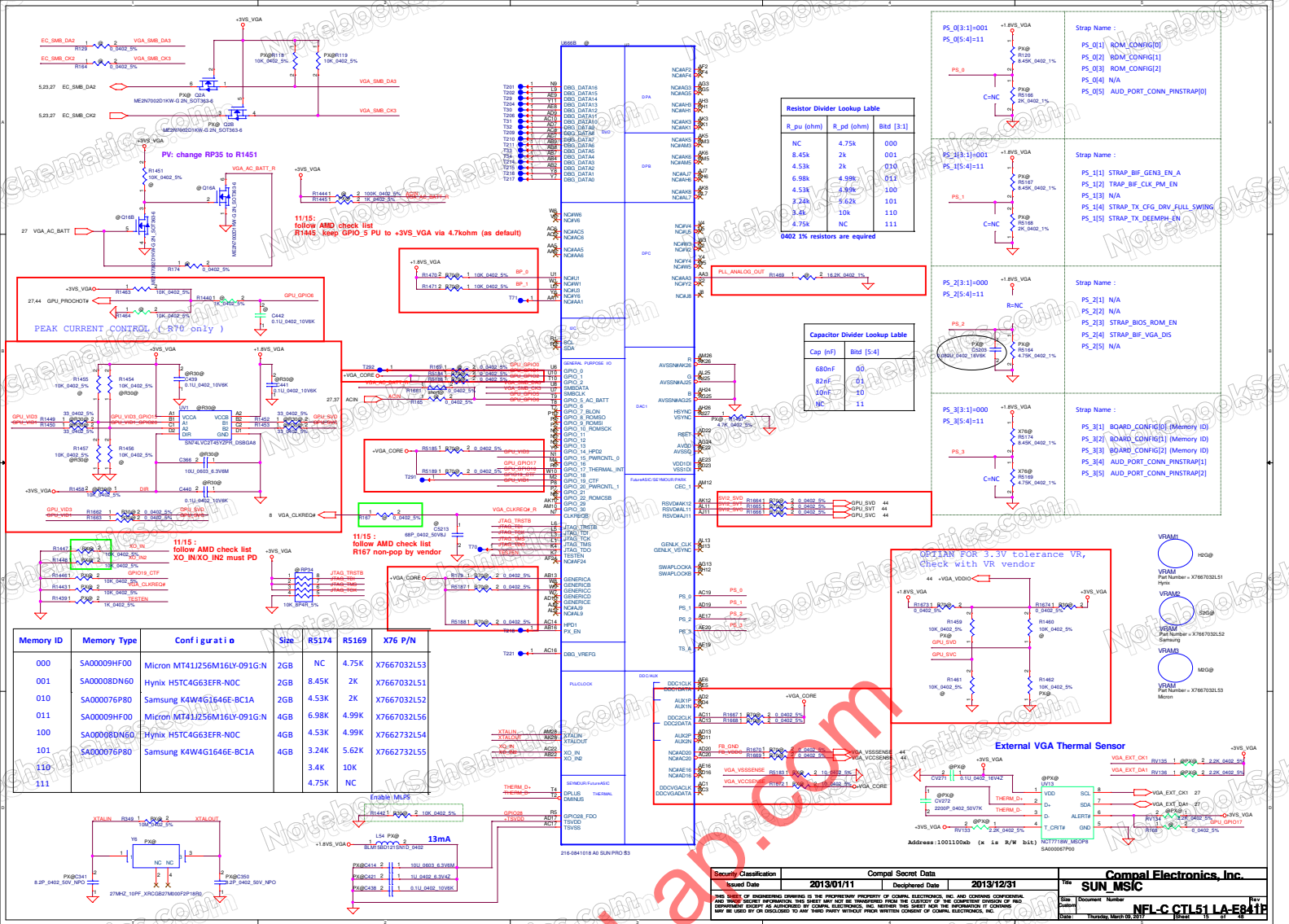


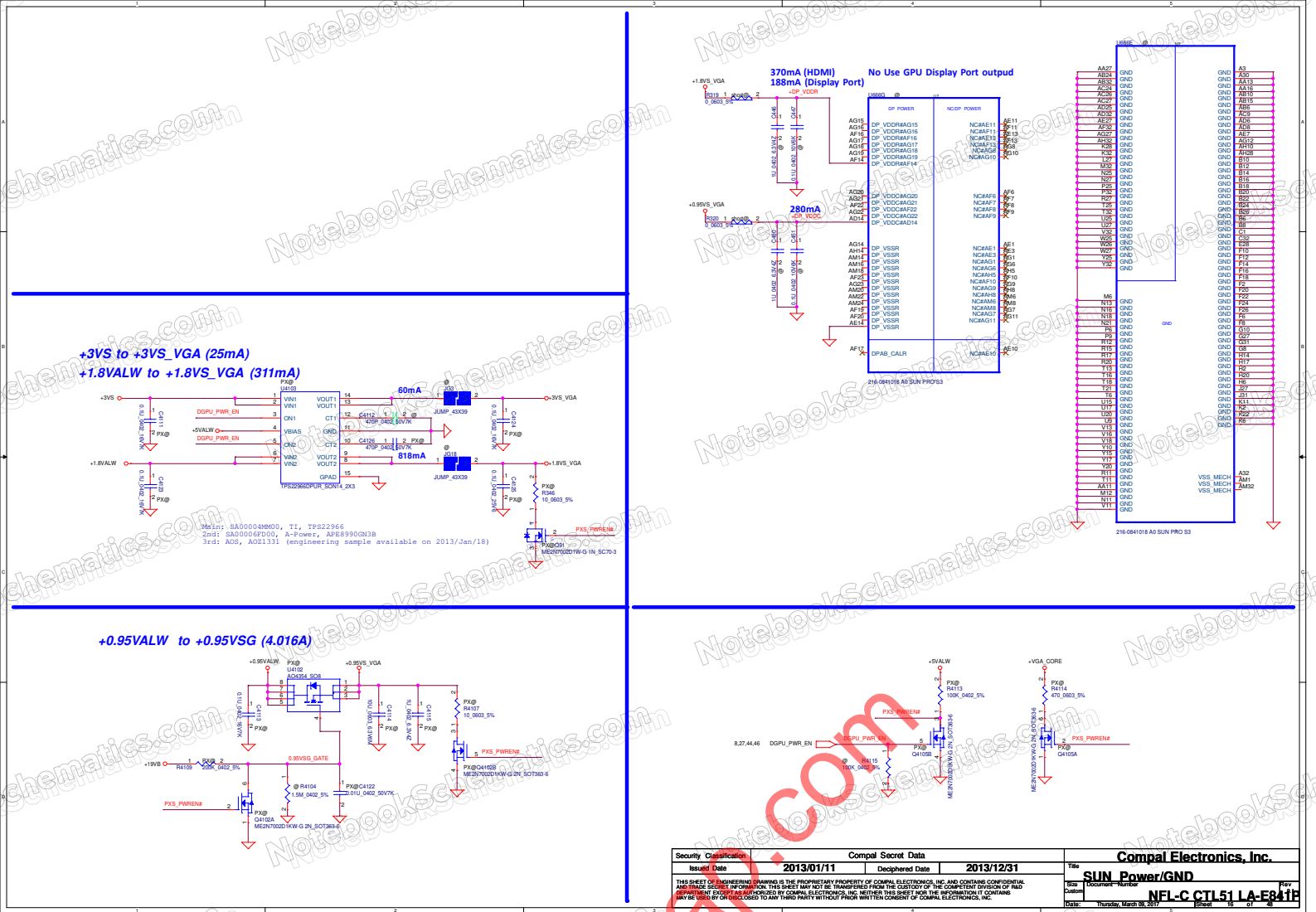




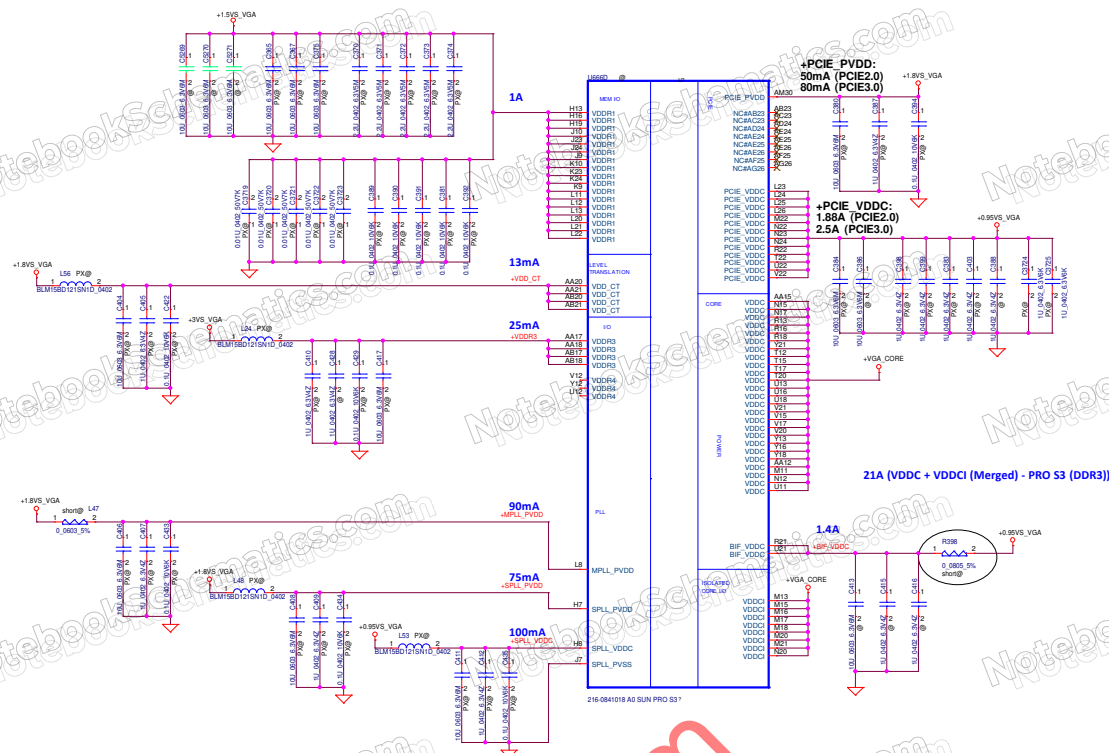


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				Rev	NFL-C CTL51 LA-E841P
				Rev	2015/09/01



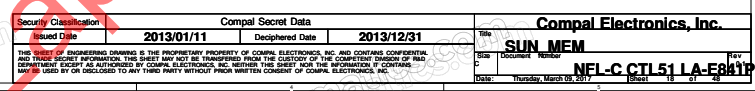


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VDDR3	25mA	0	2 (1@)	1



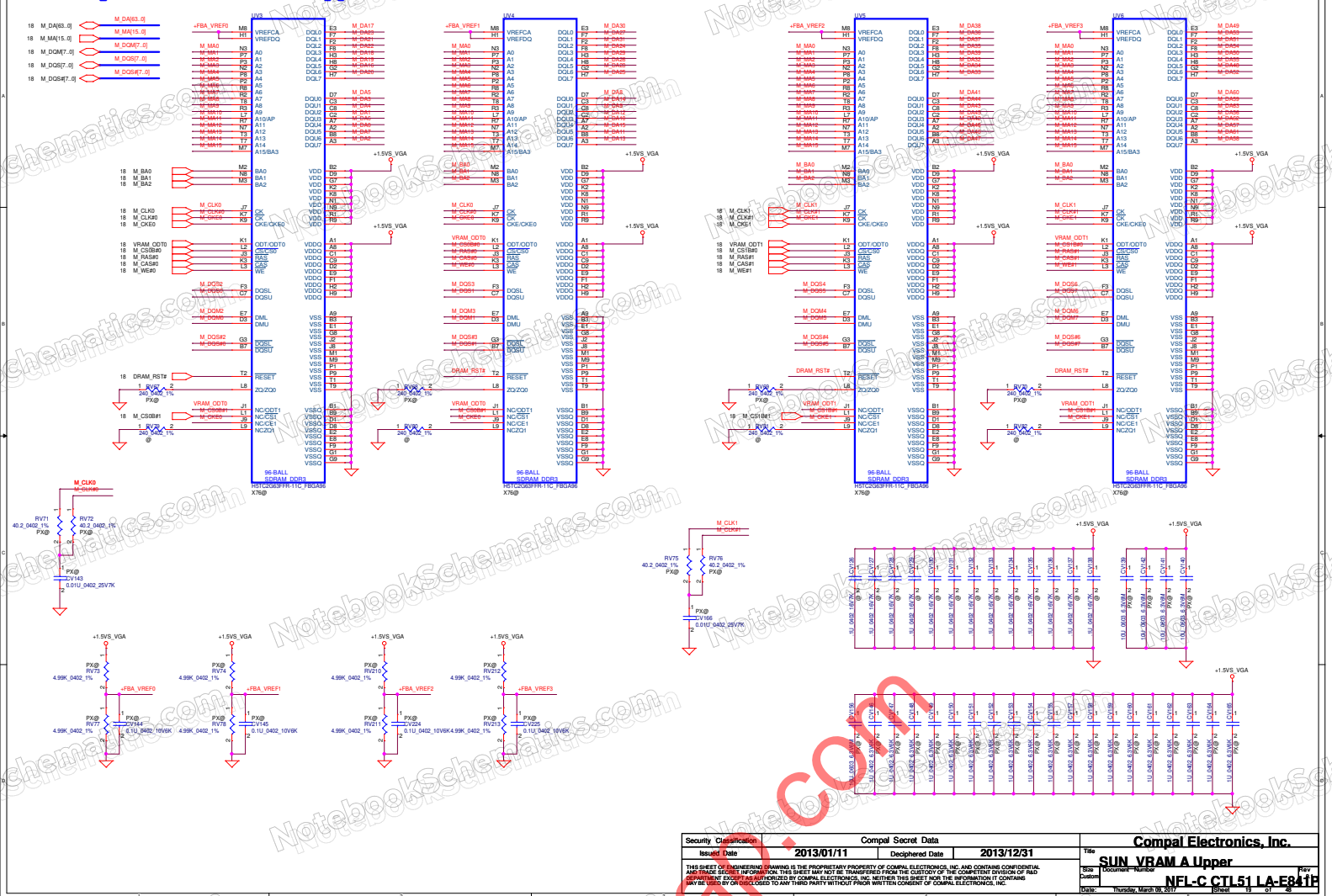
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			NFI-C CTL51 LA-E841P	



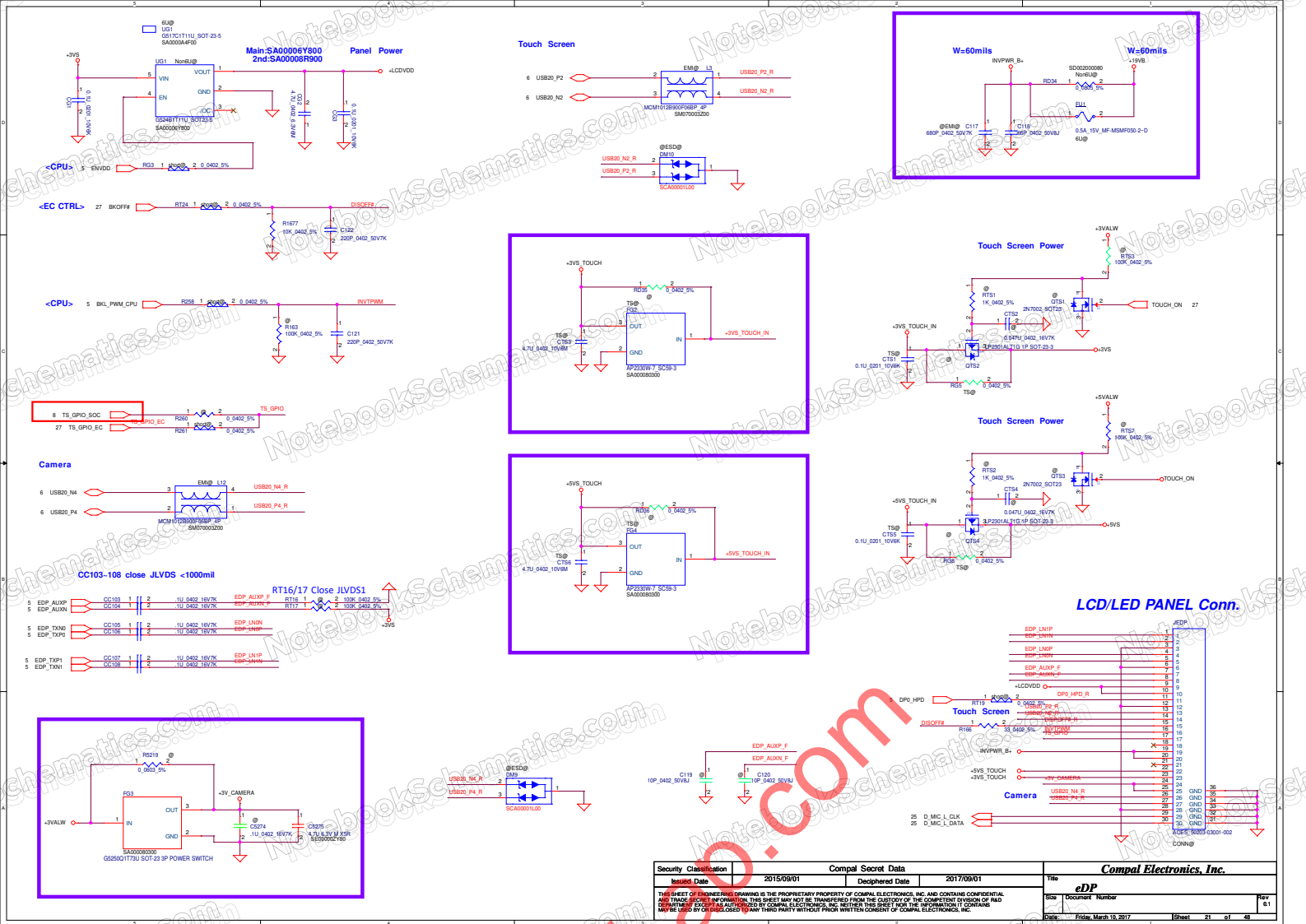




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				Doc No	NFL-C CTL51 LA-E841P
				Issue	Thursday, March 14, 2013



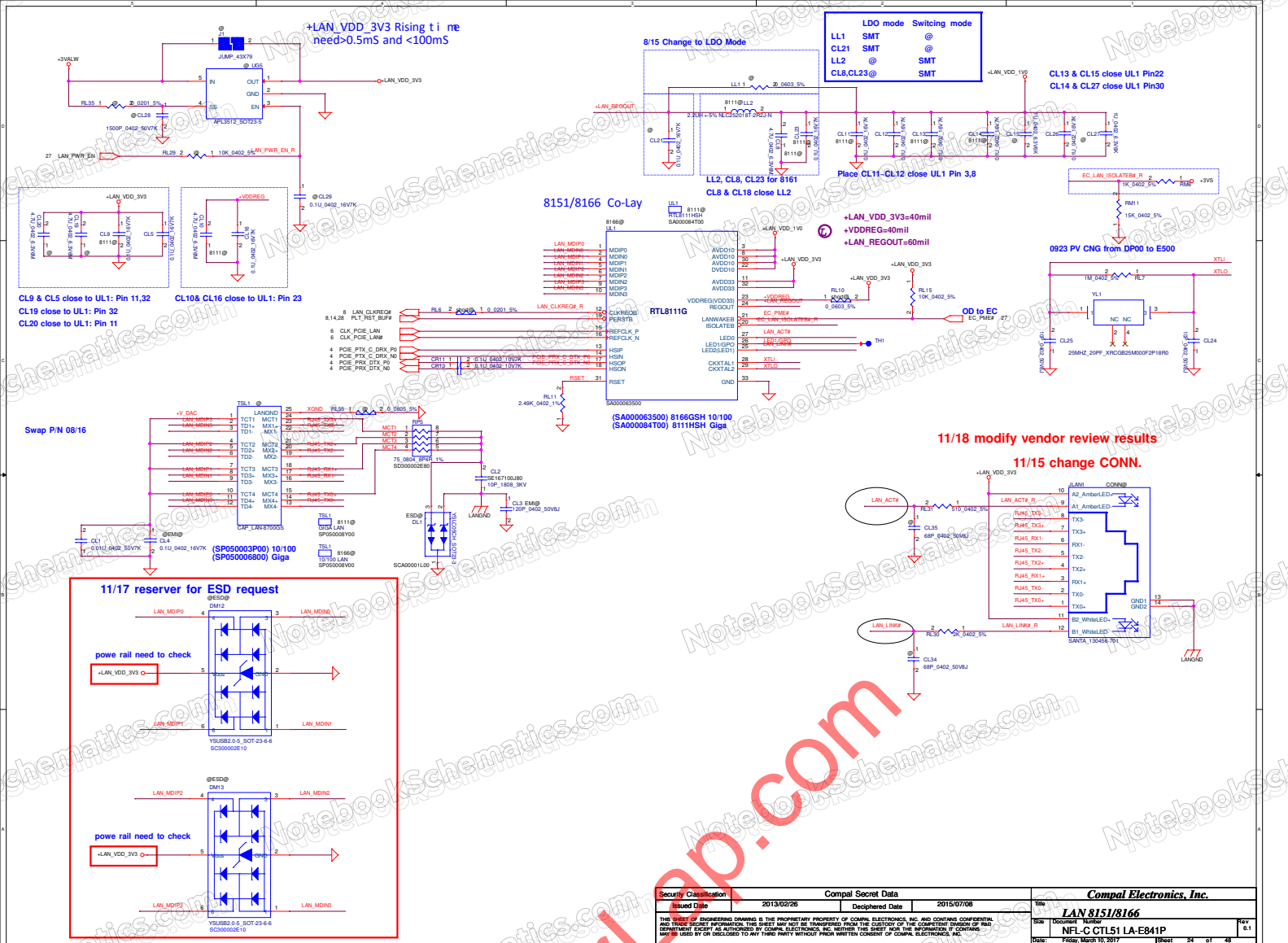


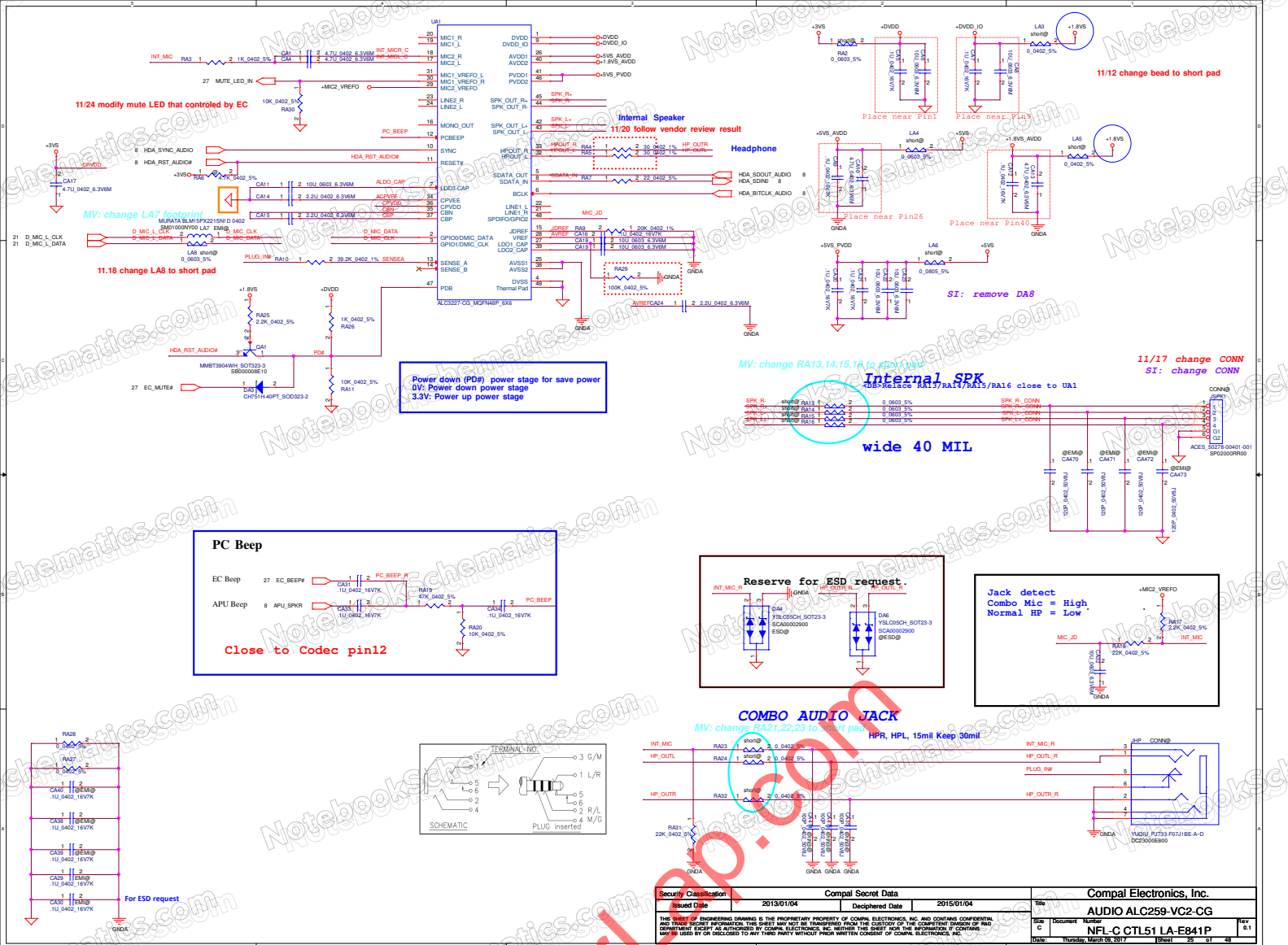


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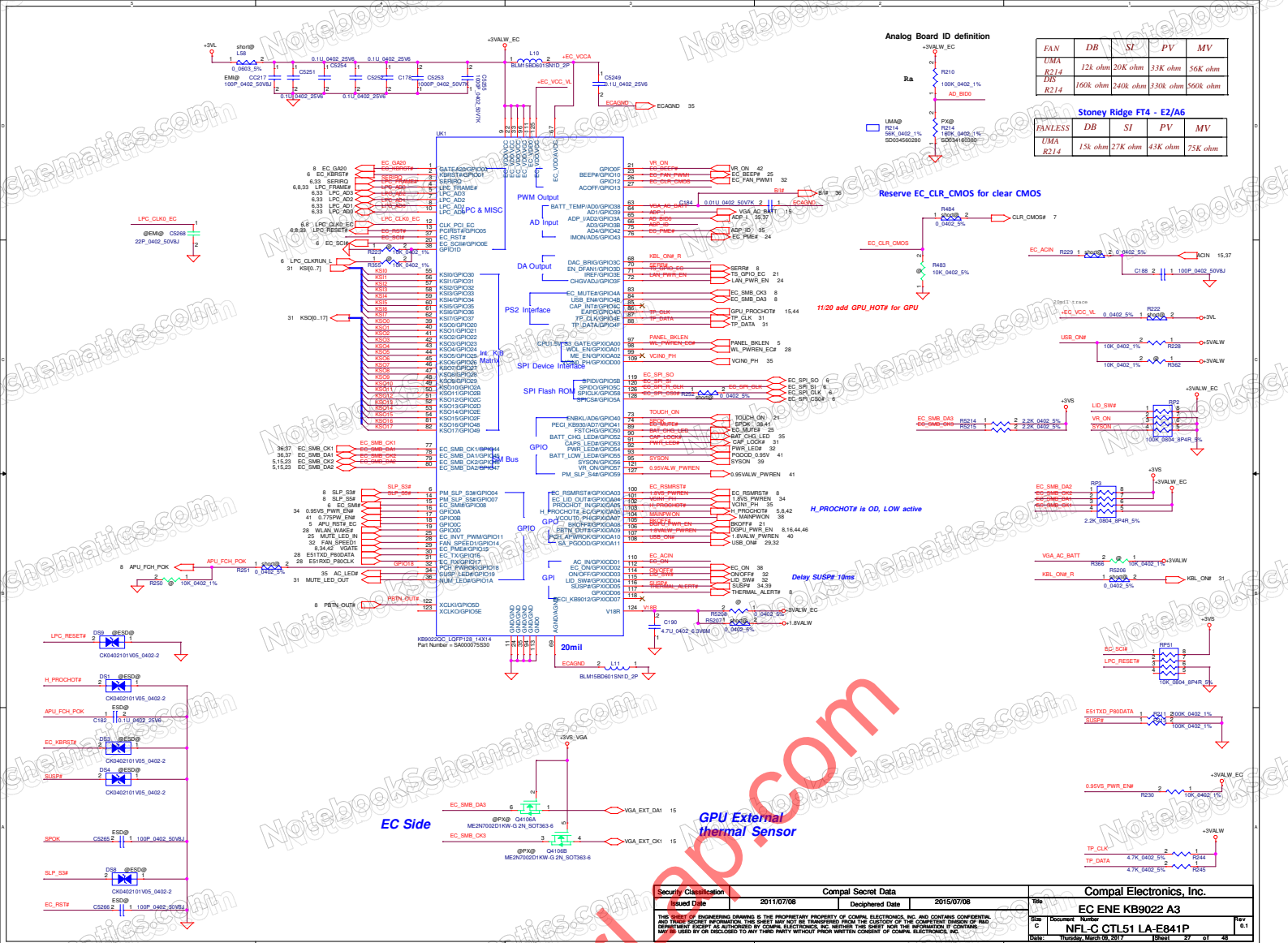
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Date	2017/03/28	Rev	01	





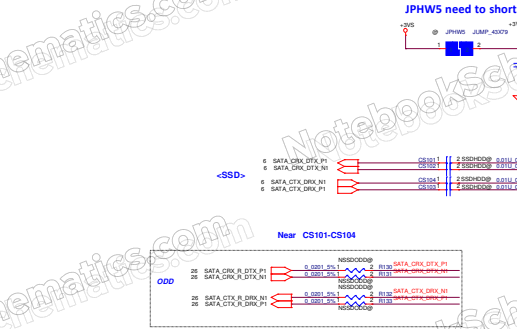








## SSD

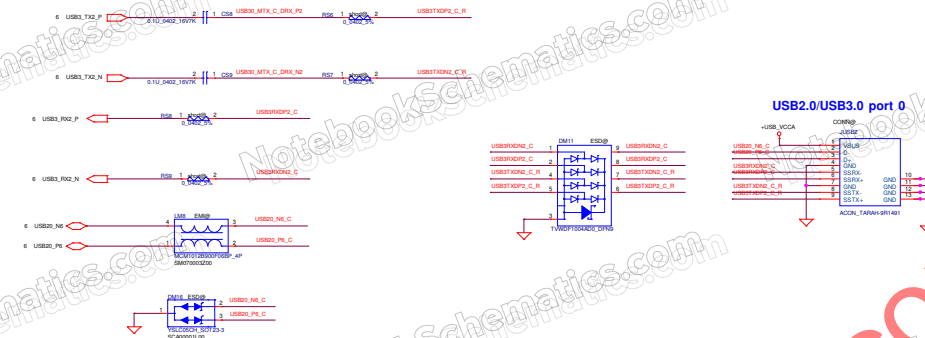


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Date: Thursday, March 29, 2017			Page	28 of 48

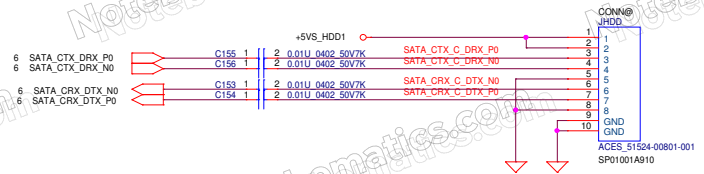
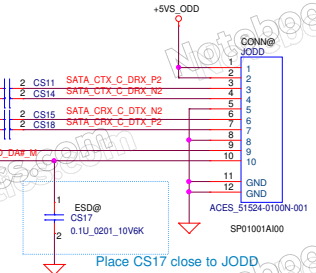
The image contains two distinct parts. The top part is a schematic diagram of a 1.5W USB Power Supply. It shows a power input section with a red arrow pointing to a terminal labeled '1.5W USB POWER SUPPLY'. This is followed by a series of electronic components: a resistor, a diode, a capacitor, and a voltage regulator. The bottom part is a security classification table with two columns. The first column is 'Security Classification' and the second is 'Issued Date'. The table contains one row with the value '2013/03/26' in the 'Issued Date' column. Below the table, there is a large block of text that is mostly obscured by a large red watermark. The visible text includes: 'THE REUSE OF INFORMATION CONTAINED IN THIS DOCUMENT MAY BE FOR THE REUSE OF INFORMATION CONTAINED HEREIN AS A SOURCE OF INFORMATION. IT IS NOT TO BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR'.

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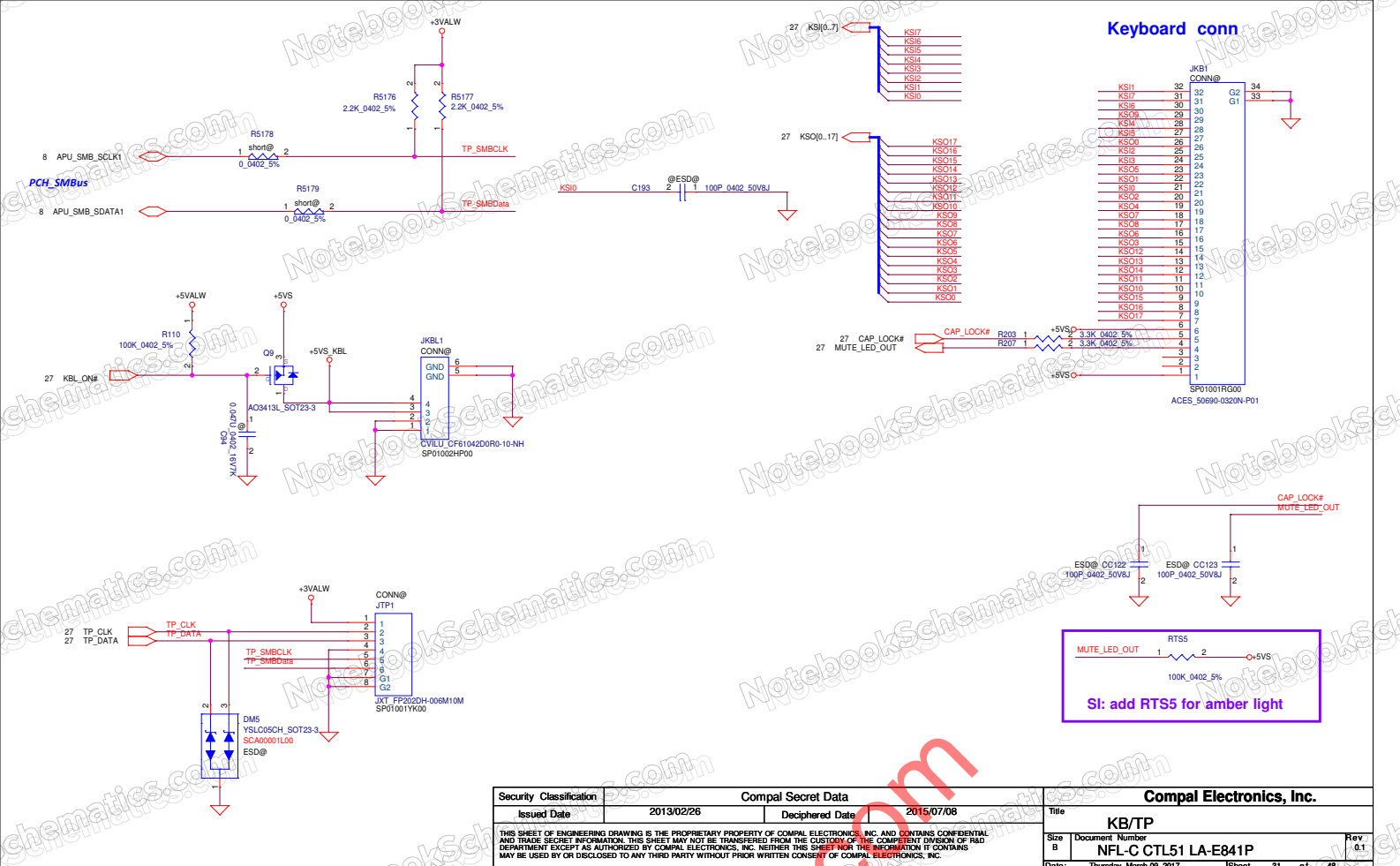
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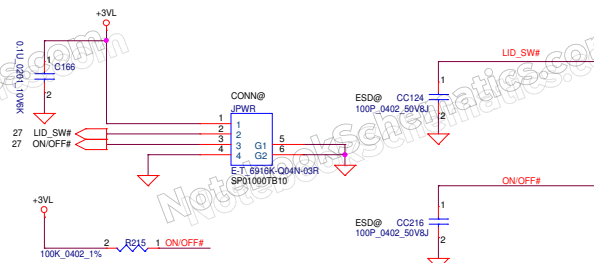
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				Size B	Document Number		Rev. 0.1	
				NPL-C CTL51 LA-E841P				
Date	Thursday, March 09, 2017		Sheet	30	of	48		

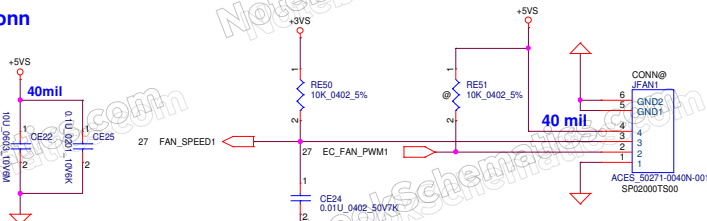




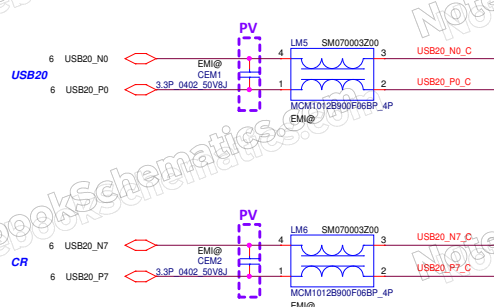
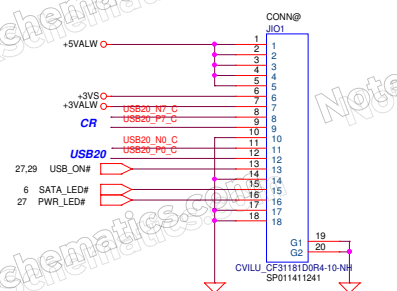
## Power Button Connector



## FAN conn

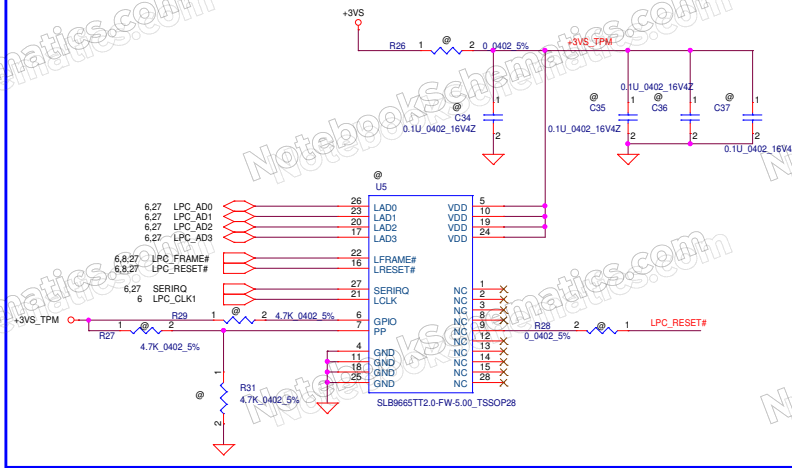


## JIO S/B

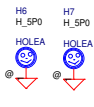


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Size B		Document Number	
NFL-C CTL51 LA-E841P		Rev	
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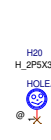
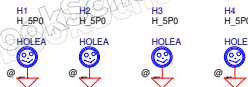
## TPM2.0



## VGA



## CPU



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## +5VS and +3VS switch

Max : 6.0A

SHORT

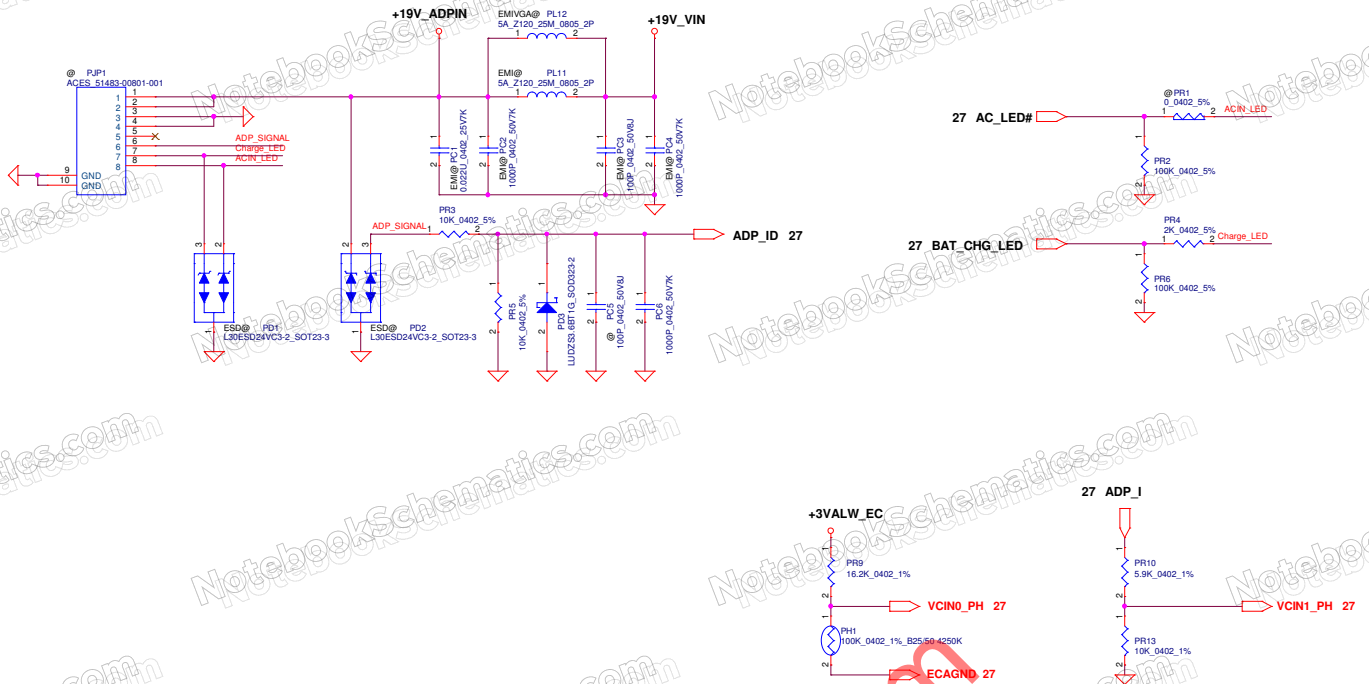
SHORT

Max : 18.0A

+1.8V TO +1.8VS (5A)

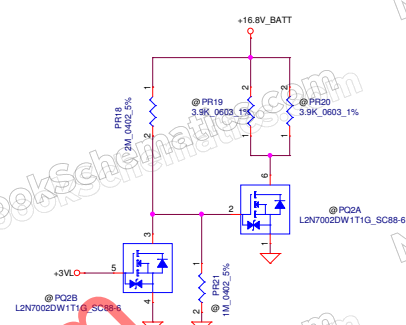
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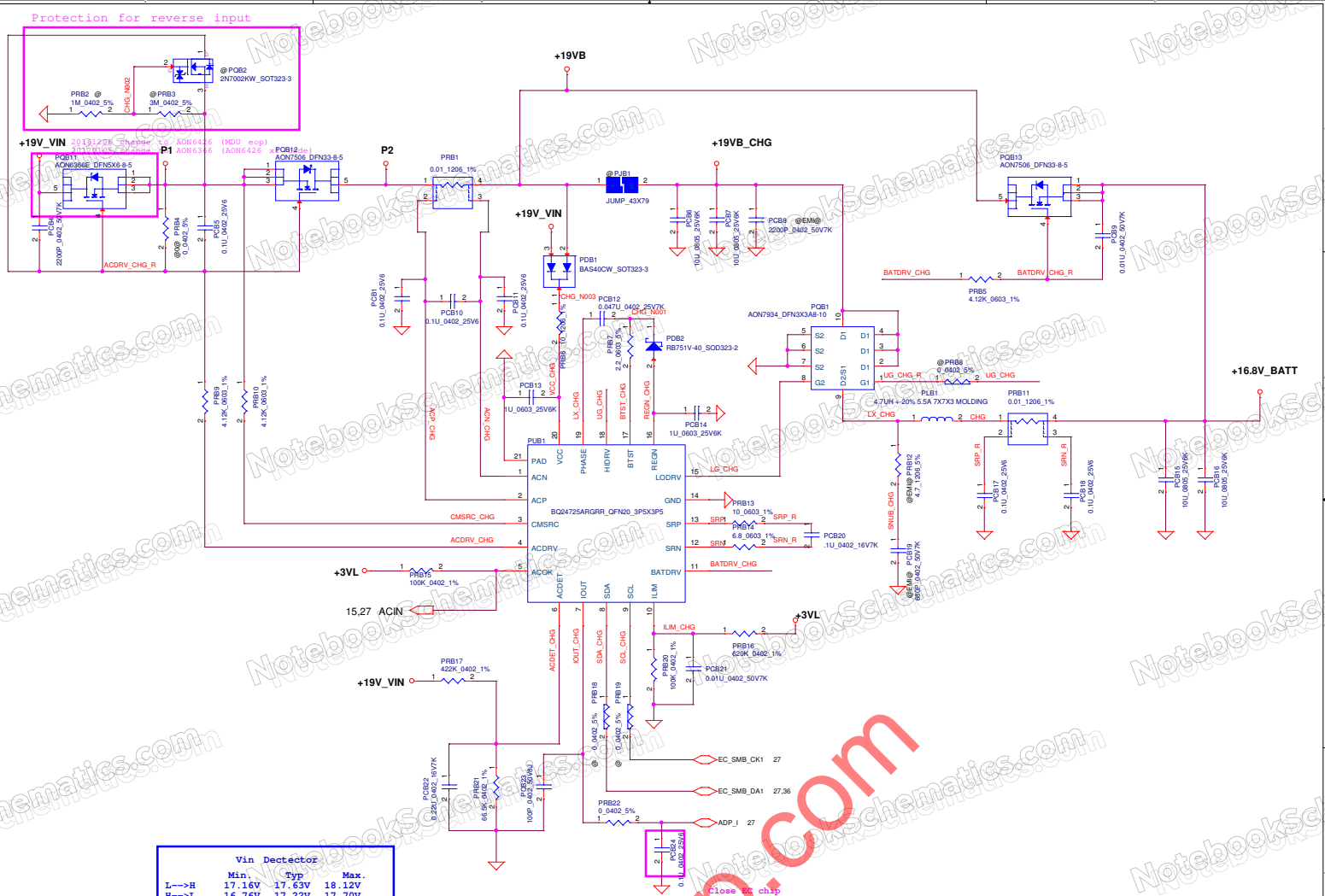


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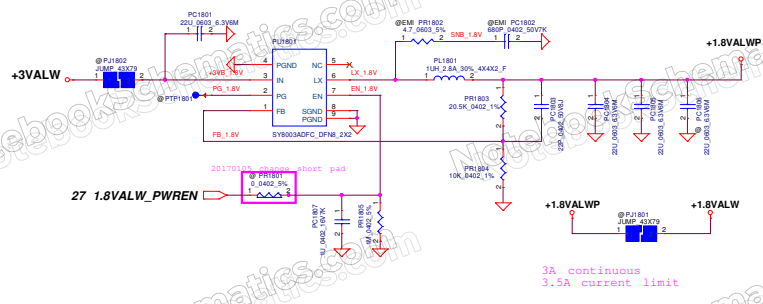


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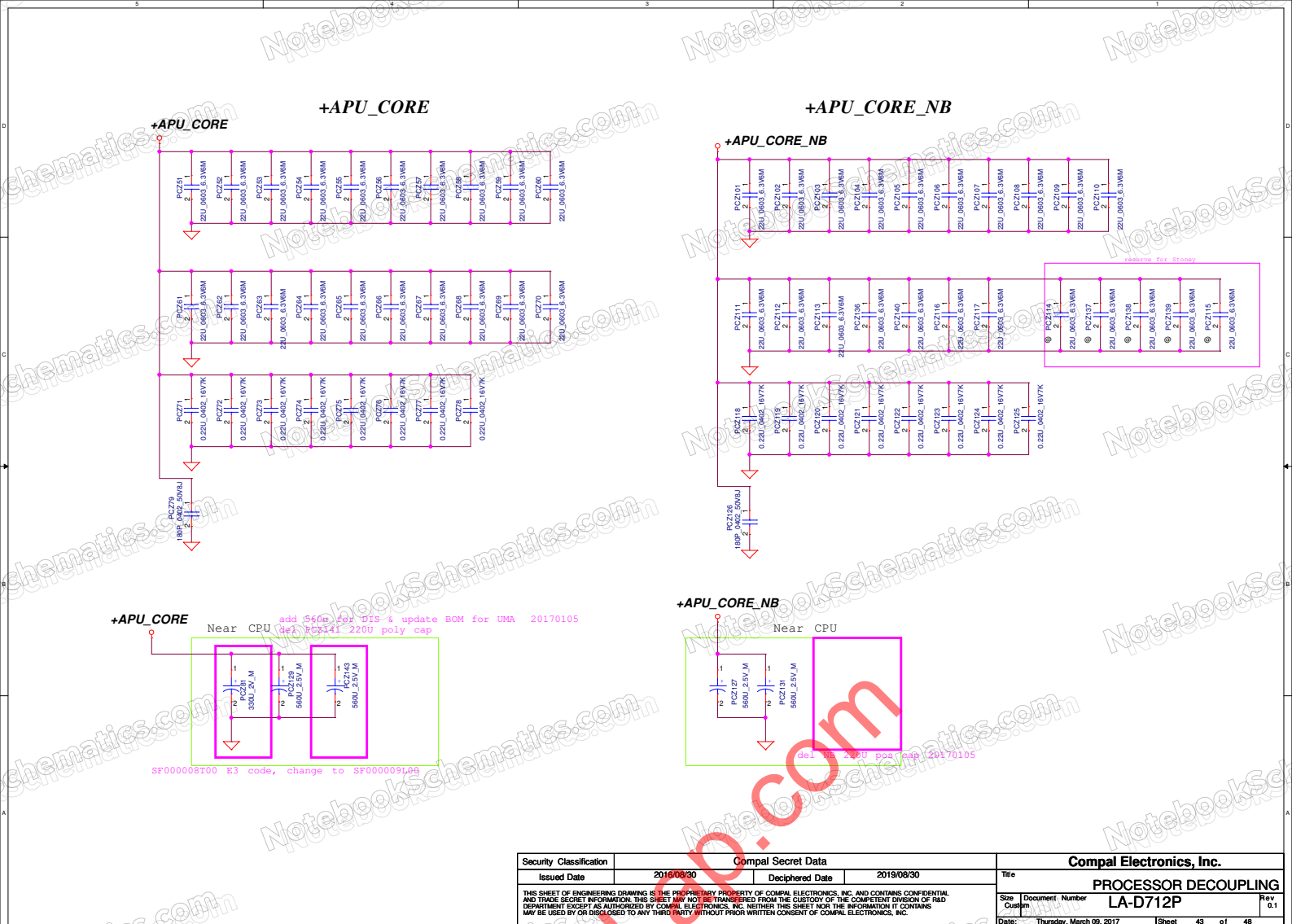




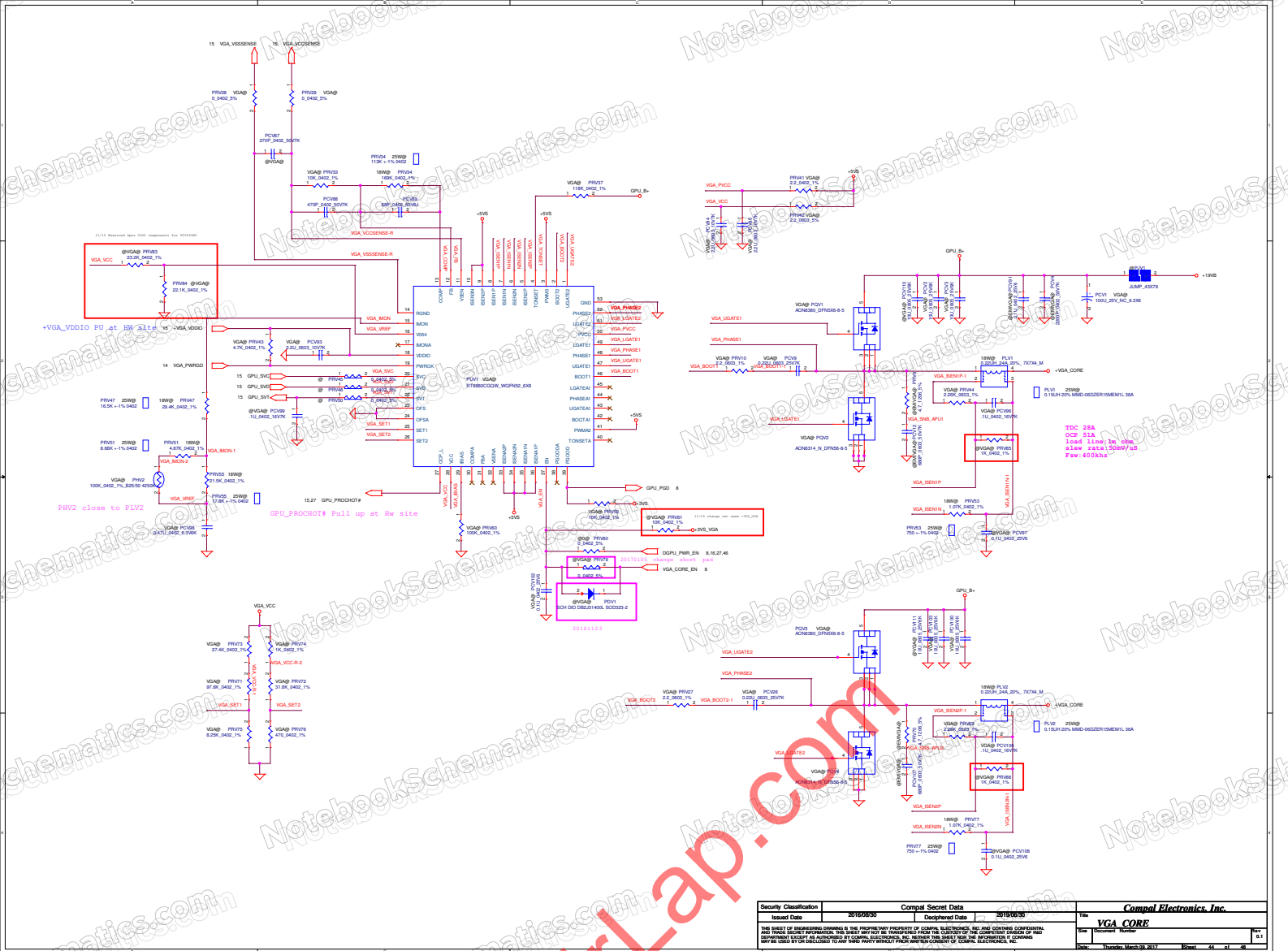
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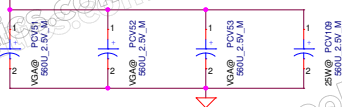




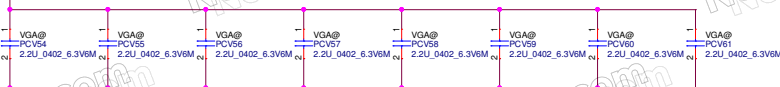




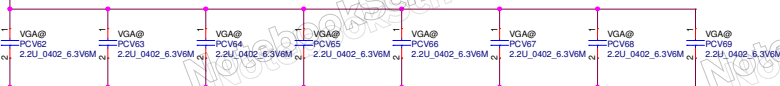
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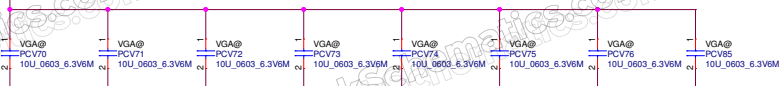
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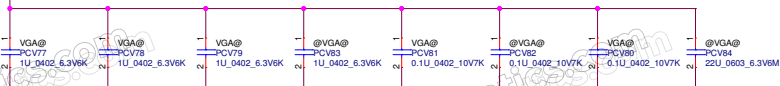
+VGA\_CORE



+VGA\_CORE



+VGA\_CORE



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## Version change list (P.I.R. List)

Item	Date	Page	Reason for change	Modify List	Phase
1	11/22	P38	layout placement	delete cap 0805 4.7U*2 (.PC323,PC324) Change cap 0805 4.7U -> 0805 10U*2 (PC321, PC322)	SI
2	11/15	P46	減少元件溫度	Location PLW2 change to PLW1 material SH00000Z200 change to SH00000YE00	SI
3	11/15	P44	layout placement	Add PRV81, PRV83, PRV84 for RT3663	SI
4	11/23	P44 P46	Issue debug	Add PDV1 and PDW1 for VGA power down sequence	SI
5	11/25	P36	update Battery connector	update Battery connector	SI
6	0105	P43	for transient test	add 560u for DIS & update BOM for UMA PCZ141	SI
7	0105	P42	for transient test	PRZ10 64.9K --> 86.6K PCZ16 270P --> 330P	PV
8	0105	P43	for transient test	PRZ10 64.9K --> 86.6K PCZ16 270P --> 330P	PV
9	0105	P44	change short pad	PR1801/ PR2501/ PRF4 PRM8/ PRM12/ PRV79/PRW1	PV
10	0105	P37	materialstorage	20161206 change to AON6426 (MDU eop) 20170105 change to AON6366 (AON6426 x1 code)	PV
11	0105	P41	follow AMD spec	resave 0.775V	PV
12	0105	P38	DFX request change footprint	PL301 11/15 change footprint L15X5X3_M_2P 12/30 change footprint CYNTE_PCB053T-3R3MS_2P	PV

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